

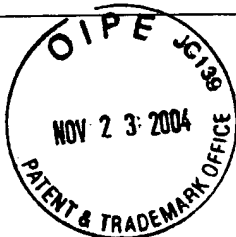
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Kamiya et al.

Serial No.: 09/935,686

Filed: 8/24/2001

Title: COMPILER FOR GENERATING
RISC OBJECT CODE WHEREBY
OPERATIONS ON BIT VARIABLES
WRITTEN IN SOURCE CODE ARE
EXECUTED BY PROCESSING BASED
ON BIT JUDGEMENT OPERATIONS TO
THEREBY REDUCE THE AMOUNT OF
OBJECT CODE



Atty. Dkt.: 11-055

Art Unit: 2122

Examiner: Ted T. VO

Commissioner for Patents
U.S. Patent and Trademark Office
220 20th Street S.
Customer Window, Mail Stop Amendment
Crystal Plaza Two, Lobby, Room 1B03
Arlington, VA 22202

Date: 23 November 2004

AMENDMENT UNDER 37 CFR 1.111

Sir:

In response to the office action mailed 10 June 2004, please amend the application as follows:

Amendments to the Claims are reflected in the listing of claims that begins on page 2 of this paper.

Remarks begin on page 11 of this paper.

11/24/2004 HTECKLU1 00000033 09935686

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